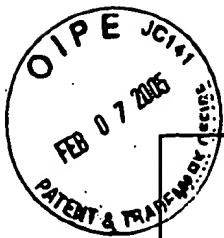


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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		4,875,881	06/1987	Uttermark			
		5,018,142	05/1991	Simeee et al.			
		5,136,587	08/1992	Obana et al.			
		5,327,126	07/1994	Beanland			
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		5,920,120	07/1999	Swenson et al.			
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		6,122,683	09/2000	Ku et al.			
		6,128,244	10/2000	Thompson et al.			
		6,134,247	10/2000	Solana de Quesada et al.			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
		42847	04/1991	KR			
		1997-56528	07/1997	KR			
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149		"21-Bit Channel Link", National Semiconductor, July 1997, http://www.national.com/ds/DS/DS90CR21.pdf					
209		Novak et al., "Channel Link Moving and Shaping Information in Point-To-Point Application", National Semiconductor, May 1996, http://www.national.com/an/AN/AN-1041.pdf .					
EXAMINER Nitin C. P. M.				DATE CONSIDERED 03-22-05			

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ref	.	von Herzen et al., "Multi-Chanel 622 Mb/s LVDS Data Transfer for Virtex-E Devices", Xilinx Inc., January 6, 2001, http://direct.xilinx.com/bvdocs/appnotes/xapp233.pdf .	
ref	.	Kitanovska et al., "Bus LVDS with Virtex-E Devices", Xilinx Inc., July 26, 2000, http://direct.xilinx.com/bvdocs/appnotes/xapp243.pdf .	

EXAMINER <i>Nitin C. Patel</i>	DATE CONSIDERED <i>9/6/05</i>
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